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1	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4)
2	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227"
3	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and analog same digital
4	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and analog same digital and (converter adcdac)
5	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and analog same digital and (converter adcdac) and (differen\$4 plural\$4 multi\$4) same power same suppl\$4
6	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and analog same digital and (converter adcdac) and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4
7	esd same (clamp\$4 protect\$4 suppress\$4) and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (converter with analog with digital adcdac) and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4
8	ground with isolat\$5 and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (converter with analog with digital adcdac) and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4
9	ground with isolat\$5 and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (converter with analog with digital adcdac) and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and (gate esd) with protect\$4
10	ground with isolat\$5 and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) same (power voltage) same analog same digital
11	ground with isolat\$5 and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) same (power voltage) same analog same digital same (over adj voltage overvoltage esd gate) same (clamp\$4 protect\$4 suppress\$4)
12	ground with isolat\$5 and diode same (clamp\$4 protect\$4 suppress\$4) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) same (power voltage) same (mixed hybrid) same (over adj voltage overvoltage esd gate) same (clamp\$4 protect\$4 suppress\$4)
13	ground same (protect\$4 diode) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4
14	ground same (protect\$4 diode) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and interface\$4
15	ground same (protect\$4 diode) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and interface\$4 and (esd electro\$8 near4 discharg\$4)
16	ground same (protect\$4 diode) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and interface\$4 and (esd electro\$8 near4 discharg\$4) and diode with protect\$5

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17	ground same (protect\$4 diode) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and interface\$4 and (esd electro\$8 near4 discharg\$4) and diode with (protect\$5 clamp\$4) with (overvoltage over adj voltage)
18	analog same digital and @ad<"20030227" and (differen\$4 plural\$4 multi\$4) with (power voltage) with suppl\$4 and interface\$4 and (esd electro\$8 near4 discharg\$4) and diode same (protect\$5 clamp\$4) same (overvoltage over adj voltage)
19	(hybrid mixed) and @ad<"20030227" and (differen\$4 plural\$4 multi\$4 mixed) with (power voltage) with suppl\$4 and interface\$4 and (esd electro\$8 near4 discharg\$4) same (protect\$5 clamp\$4) same (overvoltage over adj voltage)
20	esd and analog same digital and "716"/\$.ccls.
21	esd and analog same digital and "716"/\$.ccls. and (esd diode) same (over\$8 clamp\$3 protect\$7)
22	esd and analog same digital and "716"/\$.ccls. and (esd diode) same (over\$8 clamp\$3 protect\$7) and (differen\$4 plural\$4 multi\$4 mixed) with (power voltage) with suppl\$4
23	first near2 ground same second near2 ground same (isolat\$4 protect\$4 shunt\$5)
24	first near2 ground same second near2 ground same (isolat\$4 protect\$4 shunt\$5) same (voltage overvolatge (over exceed\$4) near3 volatge)
25	first near2 ground same second near2 ground same (isolat\$4 protect\$4 shunt\$5) same (voltage overvolatge (over exceed\$4) near3 voltage) and (interface)